

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Xiaowei Deng
 Serial No: 09/932,763
 Examiner: Daniel D. Chang
 Filed: 08/16/2001
 For: SILICON-ON-INSULATOR DYNAMIC LOGIC

Docket No: TI-29320
 Conf. No: 5414
 Art Unit: 2819



RECEIVED
 DEC 19 2002
 TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents
 Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
 I hereby certify that the above correspondence is being
 deposited with the U.S. Postal Service with sufficient
 postage as First Class Mail in an envelope addressed
 to: Assistant Commissioner for Patents, Washington,
 DC 20231 on 12-10-02.

Ann Trent

Dear Sir:

Transmitted herewith is an amendment in this application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED						
	Claims Remaining After Amendment		Highest Number Previously Paid For	Present Extra	Rate	Additional Fee
Total Claims	19	Minus	20	0	x \$18 =	\$ 0
Ind. Claims	9	Minus	4	5	x \$84 =	\$ 420
TOTAL ADDITIONAL FEE FOR THIS AMOUNT						\$ 420

Under 37 C.F.R. § 1.16(k) please charge the total additional fee, and any further fees, or credit overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. **This form is submitted in duplicate.**

Respectfully submitted,

Texas Instruments Incorporated
 PO Box 655474, M/S 3999
 Dallas, TX 75265
 (972) 917-4258

Peter K. McLarty
 Reg. No. 44,923
 Agent for Applicant